## PATENT COOPERATION TREATY



# **PCT**

## INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

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Applicant's or agent's file reference 310200945971	FOR FURTHER ACTION		cation of Transmittal of International Examination Report (Form PCT/IPEA/416)				
International application No. PCT/JP2002/008757	International filing date (d 29 August 2002 (2	•	Priority date (day/month/year)				
International Patent Classification (IPC) or G06K 19/00, G11C 16/04		,					
Applicant	RENESAS TECHNO	LOGY CORP	•				
and is transmitted to the applicant	according to Article 36.		national Preliminary Examining Authority				
This report is also accompa amended and are the basis 70.16 and Section 607 of the	anied by ANNEXES, i.e., she	ets of the descript ontaining rectific s under the PCT).	ion, claims and/or drawings which have been ations made before this Authority (see Rule				
This report contains indications re	elating to the following items:	:	· · · · · · · · · · · · · · · · · · ·				
I Basis of the repor	I Basis of the report  Π Priority						
Π Priority							
III Non-establishmer	Non-establishment of opinion with regard to novelty, inventive step and industrial applicability  IV Lack of unity of invention  Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement						
IV Lack of unity of i							
V Reasoned statement citations and explanations							
VI Certain documen	its cited						
VII Certain defects in	n the international application						
VIII Certain observati	ions on the international appli	cation					
Date of submission of the demand		Date of completion	n of this report				
29 August 2002 (29.	08.2002)	07 I	December 2004 (07.12.2004)				
Name and mailing address of the IPEA/JP		Authorized officer					
Facsimile No.		Telephone No.					

Translation

International application No.

# INTERNATIONAL PRELIMINARY EXAMINATION REPORT

PCT/JP2002/008757

I. Basis	of the re	port							
1. With	regard to	the elements of the international application:*							
	the inter	international application as originally filed							
$\boxtimes$	the desc	ription:							
	pages	1-46 , as originally filed							
	pages	, filed with the demand							
	pages	, filed with the letter of							
$\boxtimes$	the clair	ns:							
12	pages	2-9, 12-13, 15-17, 19-27, 30-31 , as originally filed							
	pages	, as amended (together with any statement under Article 19							
	pages	, filed with the demand							
	pages	1, 10, 11, 14, 18, 28, 29 , filed with the letter of25 August 2003 (25.08.2003)							
$\boxtimes$	the drav	vings:							
لكا	pages								
	pages	1-46 , as originally filed , filed with the demand							
	pages	, filed with the letter of							
	he cecue	nce listing part of the description:							
<u>ا</u> ا	pages	, as originally filed							
	pages	, filed with the demand							
	pages	, filed with the letter of							
the in Thes	the lan the lan the lan or 55.3 h regard iminary e contain filed to furnish furnish The s interna	to the language, all the elements marked above were available or furnished to this Authority in the language in which hal application was filed, unless otherwise indicated under this item.  Its were available or furnished to this Authority in the following language							
in t	This rebeyond	the description, pages the claims, Nos the drawings, sheets/fig  sport has been established as if (some of) the amendments had not been made, since they have been considered to go it the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**  sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to as "originally filed" and are not annexed to this report since they do not contain amendments (Rule 70.16) then the sheet containing such amendments must be referred to under item 1 and annexed to this report.							

### INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.
PCT/JP 02/08757

v.	Reasoned statement under Article 3 citations and explanations supporting	5(2) with regard to novelty g such statement	, inventive step or industrial appl	licability;
1.	Statement			
	Novelty (N)	Claims	1-31	YES
		Claims		NO NO
	Inventive step (IS)	Claims	1-31	YES
		Claims		NO
	Industrial applicability (IA)	Claims	1-31	YES
		Claims		NO

#### 2. Citations and explanations

- Document 1: JP 2002-164449 A (Hitachi, Ltd.), 7 June 2002, entire text, all drawings,
  - & US 2002/0074594 A1, & KR 2002/042441 A
- Document 2: JP 2001-156275 A (Hitachi, Ltd.), 8 June 2001, paragraph [0049], fig. 17, 18,
  - & US 6531735 B1, & EP 1085519 A1,
  - & KR 2001/082522 A
- Document 3: WO 2001/084556 Al (Advanced Technology Materials, Inc.), 8 November 2001, entire text, all drawings, & JP 2003-532968 A, & US 6400603 Bl, & EP 1305805 Al,
  - & US 6400603 BI, & BE 1303603 AI
  - & KR 2003/014212 A
- Document 4: JP 2000-21183 A (Matsushita Electric Industrial Co., Ltd.), 21 January 2000, entire text, all drawings

## Claims 1 to 31

The inventions described in claims 1 to 31 involve an inventive step relative to documents 1 to 3 newly cited in the international preliminary examination report and document 4 cited in the international search report.

Documents 1 to 3 disclose a non-volatile memory cell having a channel region provided in between a first diffusion layer region and a second diffusion layer region

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formed on a substrate, an electric charge storage layer provided on the channel region through a first insulating film, a first gate terminal provided on the electric charge storage layer through a second insulating film, and a second gate terminal which is provided on a second channel region adjacent to the first channel region directly under the electric charge storage layer and is connected through the first gate terminal and a third insulating layer, but none of documents 1 to 3 discloses a feature wherein, without modifying the structure of the non-volatile memory cell, a switching circuit is added, and the constitution is divided into a first non-volatile memory cell wherein stored information is erased in first data length units and a second non-volatile memory cell wherein stored information is erased in second data length units; this feature results in an advantageous effect which is not predictable from the disclosures of documents 1 to 3, namely, that the first non-volatile memory is used for storage of an encryption key, and the second nonvolatile memory is used for the storage of a program.

Meanwhile, document 4 discloses the provision of an EEPROM array which can erase in units of bytes and a flash memory array, but the structure of the non-volatile memory cells differs from that of the inventions disclosed in documents 1 to 3, and thus, a person skilled in the art would not easily conceive of combining the inventions disclosed in documents 1 to 3 and document 4.